

# CURRICULUM VITAE

**Name:** V SURYANARAYANAREDDY

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## Address:

V Suryanarayanareddy,  
S/o Srinivasulu reddy,  
Buranpur village,  
Mandal-venkatachalam,  
District – Nellore,  
State –Andhra Pradesh,  
India, pin: 524320.

## Phone no:

Mobile: 8803888048  
Alternate: 9705997069

## Alternate e-mail address:

Vsnreddy.eee@audisankara.ac.in

## Personal Data:

**Name:** V Suryanarayanareddy

**DOB:** 20-09-1991

**Sex:** Male

**Nationality:** Indian

**Religion:** Hindu

## **Languages known:**

- English (Read/Write/Speak)
- Telugu (Read/Write/Speak)
- Hindi (Read/Speak)

## **Hobbies:**

- Communicating with new people.
- Playing Cricket
- Surfing internet.

## Objective:

“To secure a challenging position where I can effectively contribute my skills for an organization as professional, possessing competent Technical skills.”

## Academic Qualifications:

- **M.tech(Computer science Engineering )**  
**College:**Audisankara college of Engineering&Tech  
**Year of passing:**2025
- **Ph.D(PURSUING)**  
**College/university:** MB University
- **M.Tech. [Power Systems]**  
**College:** SVUNIVERSITY College of Engineering.  
**University:** SVUNIVERSITY  
**Percentage/Class:** 8.7  
**Year of Passing:** 2015
- **B.Tech. [Electrical & Electronics Engineering]**  
**University:** JNTUA  
**Percentage:** 76%  
**Year of Passing:** 2013.
- **Intermediate**  
**College:** Santhinikethan Junior College,  
**University:** Board of Intermediate Education, A.P  
**Percentage:** 83%  
**Year of Passing:** 2009.
- **SSC**  
**School:** Velangani matha High School,  
**University:** Board of Secondary Education, AP  
**Percentage:** 80%  
**Year of Passing:** 2007.  
Computer skill set:
- **Operating Systems:** Windows Family
- **Soft ware's** : Mat lab, PSIM
- **Office Tools** : MS Office Family

## **Work Experience:**

### **Industrial:**

- Electrical Trainee (June 2013 to November 2013)

### **Academic:**

- Worked as Software engineer in Wipro limited **from Feb 2022 November to May 2024**
- Worked as Assistant Professor in EEE Department at NBKR Institute of Science and Technology **from October 2021 November to Feb 2022**
- Worked as Assistant Professor in EEE Department at Audisankara College of Engineering **from October 2019 to November 2021**
- Worked as Academic Consultant in EEE Department at S V U Engineering College, S.V.University, Tirupati **from -October 2018 to September 2019.**
- Worked as Assistant Professor in EEE Department at Audisankara College of Engineering **from March-2016 to -Sep-2018.**

## **Subjects Taught:**

- **Python**
- **Power system -I**
- **Circuit Theory**
- **Power system Analysis**
- **Switch Gear protection**
- **Power systems -II**
- **Software Testing**
- **Electrical distribution systems**
- **Basics of electrical engineering**

## **Laboratories Handle:**

- Circuits and Network
- Python Programming
- Electrical Measurements
- Basics of Electrical Engineering

## **Publications:**

### **List of Papers published in National/International Journals:**

#### **S.No**

#### **Publication Details**

V. Suryanarayana reddy and A. S. Kannan. "Optimized fuzzy type 1 Mamdani controller to enhance transient stability of AC-power system." *Wireless Networks* (2024): 1-16.(Springer)

Kannan, A. S., and V. Suryanarayana Reddy. "GUPFC Optimum Position and Parameters for Reducing Transmission Loss Using the BAT Algorithm." *International Journal of Research in Engineering, Science and Management* 6.8 (2023): 145-149. (Scopus)

Suryanarayana Reddy, V., Immanuel, A., Mahesh, M., Kumaraswamy, I., & Vishnuvardhan, P. (2024, July). Power Loss Reductions and Voltage profile improvement through Fuzzy and AO algorithm for effective system operation by optimal placement of UPFC. In *E3S Web of Conferences* (Vol. 547, p. 01010) (Scopus)

Anjum, S. V., Suryanarayanareddy, V., Mounika, M. L., Yamini, N., & Sandeep, M. S. (2024). Enhancing Power Quality in Integrated PV Systems and DFIG Systems through MPPT and Fuzzy logic controller for Grid Systems. In *E3S Web of Conferences* (Vol. 547, p. 01005). EDP Sciences (Scopus)

Arun Kumar, T., V. Suryanarayana Reddy, P. Dhana Selvi, B. Krishnakanth, and G. Sudeep. "PSO and GSS algorithms are used to arrange DG optimally for voltage profile enhancement and loss reduction." In *E3S Web of Conferences*, vol. 547, p. 01013. 2024.(Scopus)

Dhayanandh, S., A. Immanuel, V. Suryanarayanareddy, and K. C. Ramakrishnan. "Development of Soft Computing Technologies to Improve the Performance of Three-Phase Induction Motors." In *2022 6th International Conference on Computing Methodologies and Communication (ICCMC)*, pp. 668-674. IEEE, 2022.

Dhayanandh, S., K. C. Ramakrishnan, V. Surya Narayana Reddy, and S. Udhaya Bhaskar. "Impact of the Power Grid on Charging Stations of Various Power Rating and Sizes." *REVISTA GEINTEC-GESTAO INOVACAO E TECNOLOGIAS* 11, no. 3 (2021): 1589-1605. (ESCI)

**V.Suryanarayanareddy** and Prof A.Lakshmi devi, "Optimal placement of Interline Power Flow Controller in the transmission line using SLF algorithm" *Journal of IJRASET*, ISSN: 2348-4470, Vol. 4, Issue 11, P.P. 530-541, March-2019. (UGC Approved)

G.sudeep and V.suryanarayanareddy "Improvement of voltage stability by placing UPFC using fuzzy logic", *journal of IRJET*, ISSN:2395-0058, Vol.6, Issue 9, p.p.2395-0072, September-2019 (UGC approved)

**V. Suryanarayanareddy, Dr.A.lakshmi devi**" Minimizing the Transmission Line Loss by Using Interline Power Flow Controller", *International Journal of Engineering Trends and Technology (IJETT)*, V27(5),269-273 September 2015. ISSN:2231-5381. www.ijettjournal.org. published by seventh sense research group. (UGC Approved)

**V. suryanarayanareddy, Dr .A. Lakshmi devi** " optimal location of IPFC for transmission line loss reduction using genetic algorithm", " *International Journal of Advanced Research In Electrical and Electronics and Instrumentation Engineering (IJAREEIE)* (UGC Approved)

### **Books Published:**

- "Basic electrical and Electronics Engineering "by V.suryanarayana reddy
- "Cloud computing" by V.suryanarayana reddy
- "Electrical vehicle technology" by by V.suryanarayana reddy
- "C programming" by V.suryanarayana reddy
- "Introduction to power systems" by V.suryanarayana reddy

### **Participations and Achievements:**

#### **Conference Participations:**

- Participated and Presented as a Delegate in the "99<sup>th</sup> Annual Conference of the Indian Economics Association" S.V.University, Tirupati, during 27<sup>th</sup> to 29<sup>th</sup> December 2016 and a Paper Entitled " A Study of Economics Use and Supply of Electricity "

- Presented “**Design and Simulation of PFC Rectifier for DC Moto Drive**” in the “**83<sup>th</sup> Annual Conference of the Indian Mathematical Society – an International Meet - 2017**” held at **S.V.University, Tirupati**, during 12<sup>th</sup> to 15<sup>th</sup> December 2017.

#### **Workshop/Conference Attended:**

- Participated in Five Day International level workshop on “**Solar Photovoltaic System Design and MPPT Implementation**” organized by the Department of Electrical and Electronics Engineering, **NIT - Tiuchirapalli**, during 15<sup>th</sup> to 19<sup>th</sup> May 2019.
- Participated in One Day National level workshop on “**Machine Learning: A Hands on Tutorial using Python and Smart Transportation**” organized by **IIT Sri City, Chittoor**, during 6<sup>th</sup> October 2018.
- Participated in Three Day National level workshop on “**Industrial Applications of Electrical Engineering**” organized by the Department of Electrical and Electronics Engineering, **S.V.University, Tirupati**, during 19<sup>th</sup> to 21<sup>th</sup> July 2018.
- Participated in **Two week** national level workshop on “**Applications of Matlab in Electrical Engineering**” organized by the Department of Electrical and Electronics Engineering, **Sree Vidyanikethan Engineering College, Tirupati** during 13<sup>th</sup> to 25<sup>th</sup> June 2016.

#### **Workshop/Conference Organized:**

- Organized Member in One week national level **Conference** on “**99<sup>th</sup> All India universities Association**” **S.V.University, Tirupati**, during 5<sup>th</sup> to 7<sup>th</sup> February 2017.  
Organized Member in One week national level **Conference** on “**104<sup>th</sup> Indian Science Congress Association**” **S.V.University, Tirupati**, during 3<sup>rd</sup> to 7<sup>th</sup> January 2017.
- Organized Member in Three Day national level Conference on “**99<sup>th</sup> Annual Conference of the Indian Economics Association**” **S.V.University, Tirupati**, during 27<sup>th</sup> to 29<sup>th</sup> December 2016.

#### **FDP Attended:**

- Participated in One Week Faculty development Programme on “**Recent Trends in Power Electronics Applications in Smart Grid, Electric Vehicles and Renewable Energy**” organized by the E&ICT and Department of Electrical and Electronics Engineering, **NIT - Warangal**, during 17<sup>th</sup> to 22<sup>th</sup> June, 2019.

#### **Achievements:**

- Qualified GATE-2013,2014
- Ratified in the year 2017

#### **Memberships of professional Bodies:**

- Review member in “Journal of IJEER”, Membership No :115355(Scopus)

- Review member in “Journal of JETIR”, Membership No: 115253(Scopus)
- Review member in “journal of IRJET”
- Life Member of “**The Indian Society for Technical Education**”, Membership No: **LM 121392**

**Declaration:**

“I hereby declare that the information furnished above is true to the best of my knowledge and belief.”

**Date:**

**Place:**

(Vemireddy Suryanarayanareddy)